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A microprocessor with a 128-Bit CPU, ten **floating-point** MAC's ...

by M Suzuki - 1999 - Cited by 33 - Related articles - All 5 versions

very long instruction word (**VLIW**) **vector** processors containing ten **floating-point** multiplier accelerators and four ... **data**. The evolution of computer entertainment moves us toward the next step, ... units are **floating-point vector** processing units. One **vector** **physics** from Osaka University, Osaka, Japan, in ...

ieeexplore.ieee.org/iel5/4/17376/00799870.pdf?arnumber=799870

(WO/2005/111831) **PHYSICS** PROCESSING UNIT INSTRUCTION SET ARCHITECTURE

... **floating-point** operations on **vector** data contained in the **physics data**. The PPU of claim 34, wherein the **VLIW** comprises a first slot containing ...

www.wipo.int/pctdb/ja/la.jsp?la=US2004%2F030690&iA... - Cached

Vector processor - Wikipedia, the free encyclopedia

Oregon-based **Floating Point** Systems (FPS) built add-on array processors for centres and **physics** labs, where huge amounts of **data** are "crunched". ...

en.wikipedia.org/wiki/**Vector**_processor - Cached - Similar

Part 2 - Nicholas Blachford's even less Obsolete Home Page

The PhysX PPU (**Physics** Processor Unit) chip is made up of 3 engines along with ... For example, **data** could be written to one of the **Vector** Processing Elements ... The **Floating Point** Engine is the part of the PPU which does the real work, ... instruction and a 6 part **vector** instruction as a single **VLIW** (Very Long ...

www.blachford.info/computer/articles/PhysX2.html - Cached - Similar

[PDF] FR500 **VLIW**-architecture High-performance Embedded Microprocessor

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by VT Sukemura - 2000 - Cited by 8 - Related articles

sibility of bottlenecks in the **data** supply to the **floating-point** or media processing units. Also, a optimization technology of the **VLIW** and **vector** degree in **Physics** from Tokyo Metropol- itan University, Tokyo, Japan in 1982. ...

www.fujitsu.com/downloads/MAG/vol36-1/paper06.pdf - Similar

A 2.5-GFLOPS, 6.5 million polygons per second, four-way **VLIW** ...

by H Kubosawa - 1999 - Cited by 10 - Related articles - All 5 versions

Because **floating-point data** format is used in geometry processing and very high performance is required by the **vector** so as to activate the critical path, which is estimated **physics** from Tohoku University, Sendai, Japan, in ...

ieeexplore.ieee.org/iel5/4/17376/00799871.pdf?arnumber=799871

CS849C Lecture 2

From a **physics point** of view, an access to memory which is almost ... An other option is **VLIW**. Here, we do not try to do all the figuring out on the fly in hardware. ... **Vector** Processor - **vector** math add one **vector** (array) of **data** to another **vector** ... a load-store unit, an integer multiplier a **floating point** ALU. ...

www.cs.cmu.edu/afs/cs/academic/class/15849c.../index.html - Cached - Similar

Physics processing unit instruction set architecture - Patent ...by M Maher - 2005 - [All 5 versions](#)

This body of **data** is generically referred to hereafter as "**physics data**." Taken collectively, the units performing **floating-point vector** arithmetic ...
www.freepatentsonline.com/y2005/0251644.html

TechOnline | G4 PowerPC and C6000: A Comparison

He received a BS degree in **Physics** from Allegheny College and BSEE and MSEE degrees from ... Figure 1: Texas Instruments VelociTI core using **VLIW** instructions ... Figure 2: Motorola's G4 PowerPC core with AltiVec **vector** unit ... The C6701 can execute both fixed- and **floating-point** instructions but only six of the ...
www.techonline.com/learning/techpaper/192200649 - [Cached](#)

SIMD - Wikipedia, the free encyclopedia

The Cray X-MP **vector** processors were pipelined and had multiple functional units. ... A sliding scale exists - the Cell's SPUs and Ageia's PhysX **Physics** ... In other words, if the SIMD system works by loading up eight **data points** at once, ... of the system due to the re-use of existing **floating point** registers. ...
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